MA5201 (Preliminary)

### 1.2A/24V/1.4MHz Asynchronous Buck Converter <br> * GENERAL DESCRIPTION

The MA5201 is a monolithic asynchronous buck regulator. The device integrates power MOSFET, and provides 1.2A of continuous load current over a wide input voltage of 4.75 V to 24 V . Current mode control provides fast transient response and cycle-bicycle current limit.

An adjustable soft-start prevents inrush current at turn-on, and in shutdown mode the supply current drops to $1 \mu \mathrm{~A}$. This device, available in SOT23-6 package, provides a very compact solution with minimal external components.

## * FEATURES

- Wide 4.75 V to 24 V Operating Input Range
- Integrated $300 \mathrm{~m} \Omega$ Power MOSFET Switches
- Output Adjustable from VFB( 0.81 V ) to 12 V
- Internal Soft-Start
- Stable with Low ESR Ceramic Output Capacitors
- Fixed 1.4MHz Frequency
- Cycle-by-Cycle Over Current Protection
- Input Under Voltage Lockout


## * APPLICATION CIRCUIT



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## * PIN ASSIGNMENT

The package of MA5201 is SOT23-6L; the pin assignment is given by:


| Name | Description |
| :---: | :--- |
| BS | Boot-Strap Pin. Supply high side <br> gate driver. Decouple this pin to LX <br> pin with 0.1uF ceramic cap. |
| GND | Ground Pin. |
| FB | Output Feedback Pin. Connect this <br> pin to the center point of the output <br> resistor divider to program the output <br> voltage: Vout=0.81*(1+R1/R2) |
| EN | Enable control. Pull high to turn on. <br> Do not float. |
| IN | Input pin. Decouple this pin to GND <br> pin with at least 1uF ceramic cap |
| LX | Inductor pin. Connect this pin to the <br> switching node of inductor |

* RDER/MARKING INFORMATION

| Order Information | Top Marking |
| :---: | :---: |
|  |  |

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* BLOCK DIAGRAM

* A BSOLUTE MAXIMUM RATINGS (at $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ )

| Characteristics | Symbol | Rating | Unit |
| :--- | :---: | :---: | :---: |
| Supply Voltage | $\mathrm{V}_{\text {IN }}$ | 26 | V |
| Switch Node Voltage | $\mathrm{V}_{\text {SW }}$ | -0.3 to $\mathrm{V}_{\text {IN }}+0.3$ | V |
| Boost Voltage | $\mathrm{V}_{\text {BS }}$ | $\mathrm{V}_{\text {SW }}+6$ | V |
| All Other Pins |  | -0.3 to +6 | V |
| Lead Temperature |  | 260 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature |  | -65 to +150 | ${ }^{\circ} \mathrm{C}$ |
| Junction Temperature | $\mathrm{T}_{\mathrm{J}}$ | 150 | ${ }^{\circ} \mathrm{C}$ |
| Input Voltage | $\mathrm{V}_{\text {IN }}$ | 4.75 to 24 | V |
| Output Voltage | $\mathrm{V}_{\text {OUT }}$ | VFB to 12 | V |
| Ambient Operating Temperature |  | -40 to +85 | ${ }^{\circ} \mathrm{C}$ |
| Thermal Resistance from Junction to case | $\theta_{\text {JC }}$ | 180 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Thermal Resistance from Junction to ambient | $\theta_{\mathrm{JA}}$ | 250 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

Note: $\theta_{\mathrm{JA}}$ is measured with the PCB copper area of approximately 0.5 in$^{2}$ (Multi-layer). That need connect to GND pin.

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* ELECTRICAL CHARACTERISTICS
( $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise noted.)

| Characteristics | Symbol | Conditions | Min | Typ | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Shutdown Supply Current | ISD | $\mathrm{V}_{\mathrm{EN}}=0 \mathrm{~V}$ | - | 1 | 3.0 | $\mu \mathrm{A}$ |
| Quiescent Current | Icco | $\begin{aligned} & V_{E N}=2.5 \mathrm{~V} ; \\ & V_{F B}=0.63 \mathrm{~V} \end{aligned}$ | - | 750 | - | uA |
| Feedback Voltage | $V_{\text {FB }}$ | $4.75 \mathrm{~V} \leq \mathrm{V}_{\mathbb{N}} \leq 16 \mathrm{~V}$ | 0.794 | 0.810 | 0.826 | V |
| Feedback Overvoltage Threshold | OVP ${ }_{(F B)}$ |  | - | 1.1X | - | $V_{\text {FB }}$ |
| High-Side Switch On Resistance (Note) | Rds(on)1 |  | - | 300 | - | $\mathrm{m} \Omega$ |
| High-Side Switch Leakage Current |  | $V_{\text {EN }}=0 \mathrm{~V}, \mathrm{~V}_{\text {sw }}=0 \mathrm{~V}$ | - | - | 10 | $\mu \mathrm{A}$ |
| Switch Current Limit |  | Minimum Duty Cycle | - | 1.5 | - | A |
| Oscillation Frequency | Fosc1 |  | - | 1400 | - | KHz |
| Short Circuit Oscillation Frequency | Fosc2 | $V_{\text {FB }}=0 \mathrm{~V}$ | - | 200 | - | KHz |
| Maximum Duty Cycle | $\mathrm{D}_{\text {max }}$ |  | - | 90 | - | \% |
| Minimum On Time (Note) | Ton(min) |  | - | 100 | - | ns |
| EN Shutdown Threshold Voltage | ENH | $\mathrm{V}_{\text {EN }}$ Rising | 1.1 | 1.5 | 2.0 | V |
| EN Shutdown Threshold Voltage Hysteresis |  |  | - | 210 | - | mV |
| Input Under Voltage Lockout Threshold | UVLO | $\mathrm{V}_{\mathbb{N}}$ Rising | 3.80 | 4.10 | 4.40 | V |
| Input Under Voltage Lockout Threshold Hysteresis | UVLO-Hys |  | - | 210 | - | mV |
| Soft-Start Period |  | $C_{s s}=0.1 \mu \mathrm{~F}$ | - | 4 | - | ms |
| Thermal Shutdown | Tsd |  | - | 150 | - | ${ }^{\circ} \mathrm{C}$ |
| Thermal Shutdown Hysterisis | $\mathrm{T}_{\text {SH }}$ |  | - | 30 | - | ${ }^{\circ} \mathrm{C}$ |

Note: Guaranteed by design.

## * FUNCTION DESCRIPTIONS

The MA5201 is a synchronous rectified, current-mode, step-down regulator. It regulates input voltages from 4.75 V to 24 V down to an output voltage as low as $\mathrm{V}_{\mathrm{FB}}$, and supplies up to 1.2A of load current.

The MA5201 uses current-mode control to regulate the output voltage. The output voltage is measured at FB through a resistive voltage divider and amplified through the internal Tran conductance error amplifier. The voltage at the COMP pin is compared to the switch current measured internally to control the output voltage.

The converter uses internal N-Channel MOSFET switches to step-down the input voltage to the regulated output voltage. Since the high side MOSFET requires a gate voltage greater than the input voltage, a boost capacitor connected between SW and BS is needed to drive the high side gate. The boost capacitor is charged from the internal 5 V rail when SW is low.

When the MA5201 FB pin exceeds $10 \%$ of the nominal regulation voltage of $V_{F B}$, the over voltage comparator is tripped and the COMP pin is discharged to GND, forcing the high-side switch off.

## External Bootstrap Diode

An external bootstrap diode may enhance the efficiency of the regulator, the applicable conditions of external BST diode are:

- $V_{\text {out }}=5 \mathrm{~V}$ or 3.3 V ; and
- Duty cycle is high: $D=\frac{V_{O U T}}{V_{I N}}>65 \%$

In these cases, an external BST diode is recommended from the output of the voltage regulator to BST pin, as shown in Fig. 1


Figure 1 Add Optional External Bootstrap Diode to Enhance Efficiency
The recommended external BST diode is $\operatorname{IN} 4148$, and the BST cap is $0.1 \sim 1 \mu \mathrm{~F}$.

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* PACKAGE OUTLINES


| Symbol | Dimensions in Millimeters |  |  | Dimensions in Inches |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Min. | Nom. | Max. | Min. | Nom. | Max. |
| A | 1.05 | - | 1.45 | 0.041 | - | 0.057 |
| A1 | 0.05 | - | 2.15 | 0.002 | - | 0.085 |
| A2 | 0.90 | 1.10 | 1.30 | 0.035 | 0.043 | 0.051 |
| b | 0.30 | - | 0.50 | 0.012 | - | 0.020 |
| C | 0.08 | - | 0.22 | 0.003 | - | 0.009 |
| D | 2.70 | 2.90 | 3.10 | 0.106 | 1.114 | 0.122 |
| E1 | 1.40 | 1.60 | 1.80 | 0.055 | 0.063 | 0.071 |
| E | 2.60 | 2.80 | 3.00 | 0.102 | 0.110 | 0.118 |
| L | 0.30 | - | 0.60 | 0.012 | - | 0.024 |
| L1 | 0.50 | 0.60 | 0.70 | 0.020 | 0.024 | 0.028 |
| e1 | 1.80 | 1.90 | 2.00 | 0.071 | 0.075 | 0.079 |
| e | 0.85 | 1.00 | 1.15 | 0.033 | 0.037 | 0.045 |
| $\theta$ | $0^{\circ}$ | 40 | $8^{\circ}$ | $0^{\circ}$ | 40 | $8^{\circ}$ |

